

FIG. 1

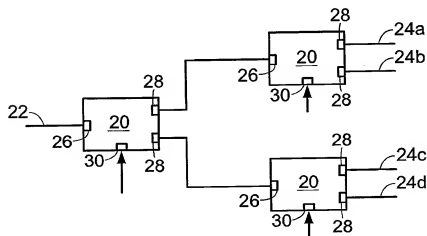


FIG. 2

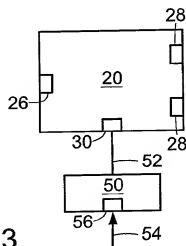


FIG. 3

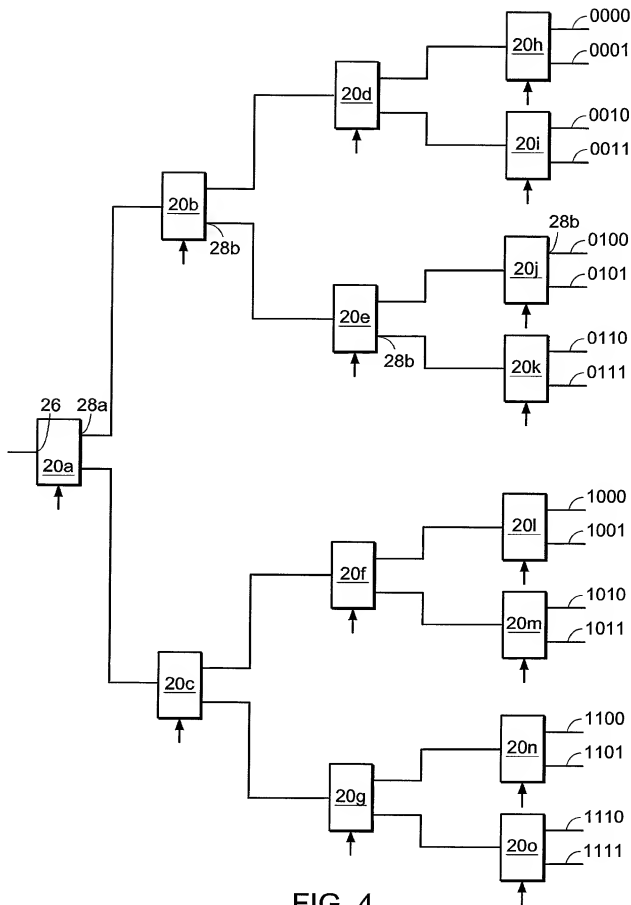


FIG. 4



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	a	b	c	d	e	f	g	h	i	j	k	l	m	n	o
0000	I	I	X	I	X	X	X	I	X	X	X	X	X	X	X
0001	I	I	X	I	X	X	X	O	X	X	X	X	X	X	X
0010	I	I	X	O	X	X	X	X	I	X	X	X	X	X	X
0011	I	I	X	O	X	X	X	X	O	X	X	X	X	X	X
0100	I	O	X	X	I	X	X	X	X	I	X	X	X	X	X
0101	I	O	X	X	I	X	X	X	X	O	X	X	X	X	X
0110	I	O	X	X	O	X	X	X	X	X	I	X	X	X	X
0111	I	O	X	X	O	X	X	X	X	X	O	X	X	X	X
1000	O	X	I	X	X	I	X	X	X	X	X	I	X	X	X
1001	O	X	I	X	X	I	X	X	X	X	X	O	X	X	X
1010	O	X	I	X	X	O	X	X	X	X	X	X	I	X	X
1011	O	X	I	X	X	O	X	X	X	X	X	O	X	X	X
1100	O	X	O	X	X	X	I	X	X	X	X	X	X	I	X
1101	O	X	O	X	X	X	I	X	X	X	X	X	X	O	X
1110	O	X	O	X	X	X	O	X	X	X	X	X	X	X	I
1111	O	X	O	X	X	X	O	X	X	X	X	X	X	X	O

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"I" - TOP PORT
 "O" - BOTTOM PORT
 "X" - "DON'T CARE"

FIG. 5

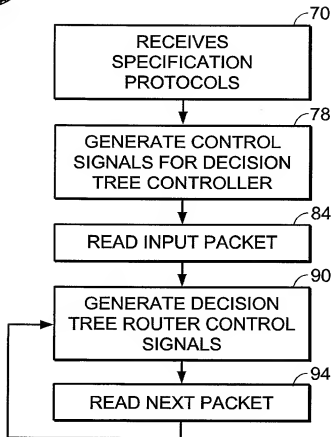


FIG. 6

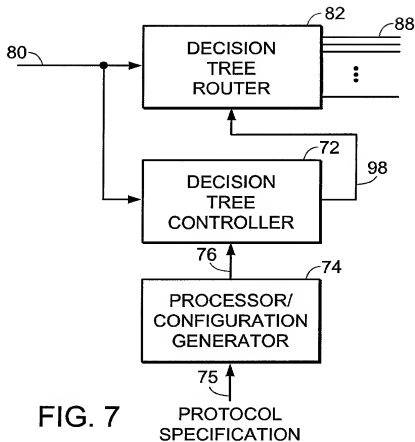


FIG. 7